

CP/M 2.2C

Schaltplan (EACA)

Stand: 01.01.84

Anlage C:

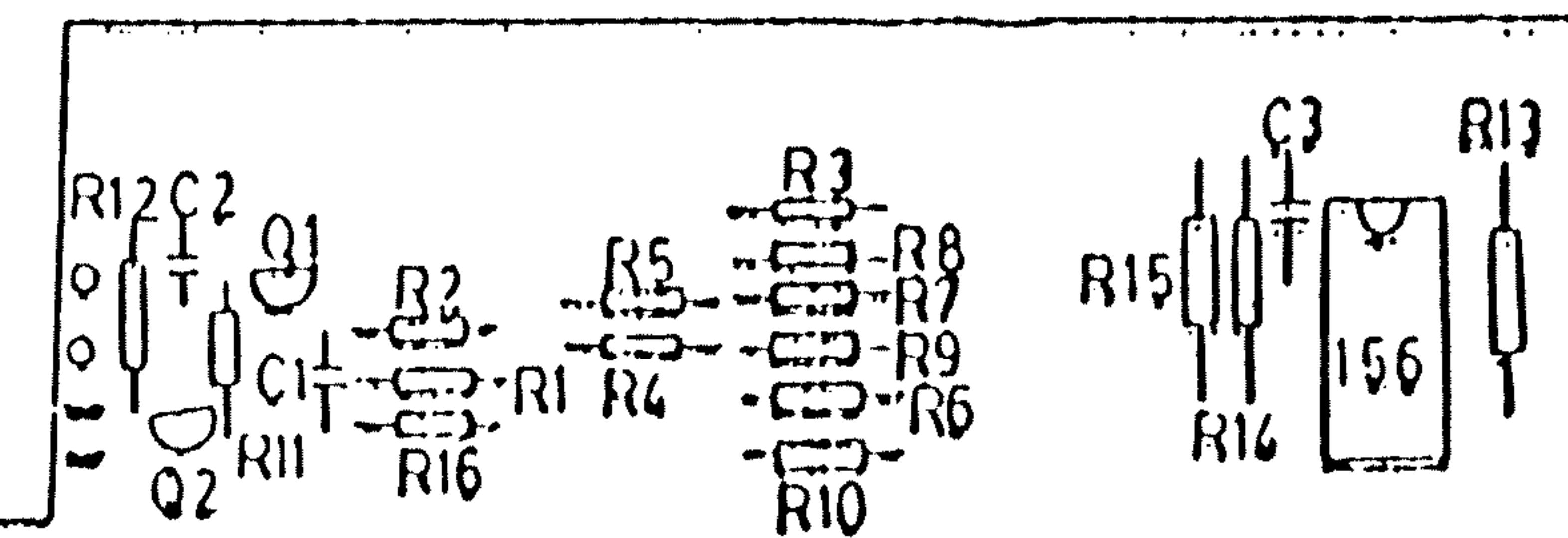
Schaltplan GENIE III

(C) by EACA

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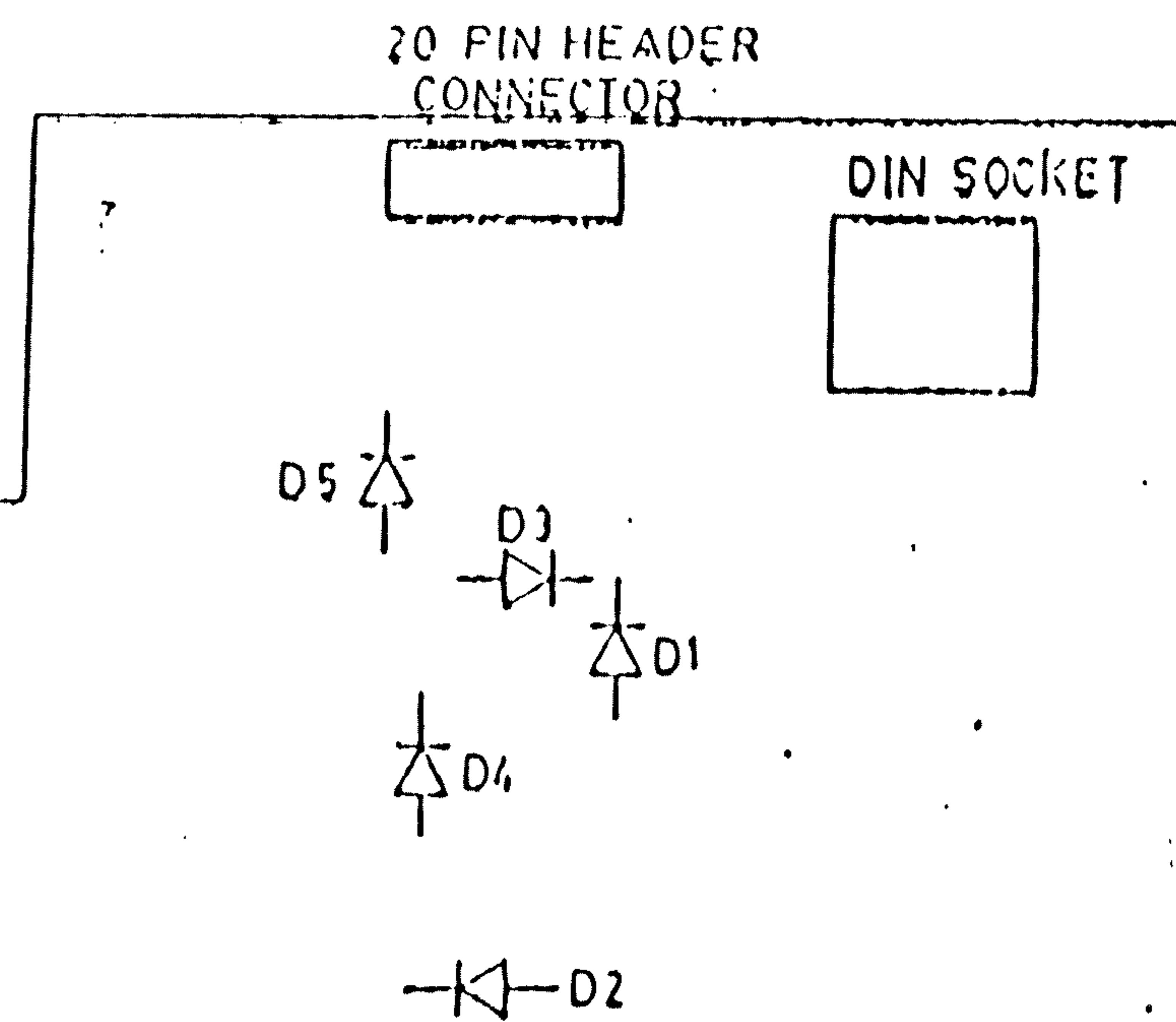
COMPONENT SIDE

KEY BOARD COMPONENT LAYOUT

PAGE 1 OF 3


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SOLDERING SIDE

KEY BOARD COMPONENT LAYOUT
PAGE 2 OF 3



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
18 LIGHTPEN

GENIE II KEYBOARD

PAGE 3 OF 3

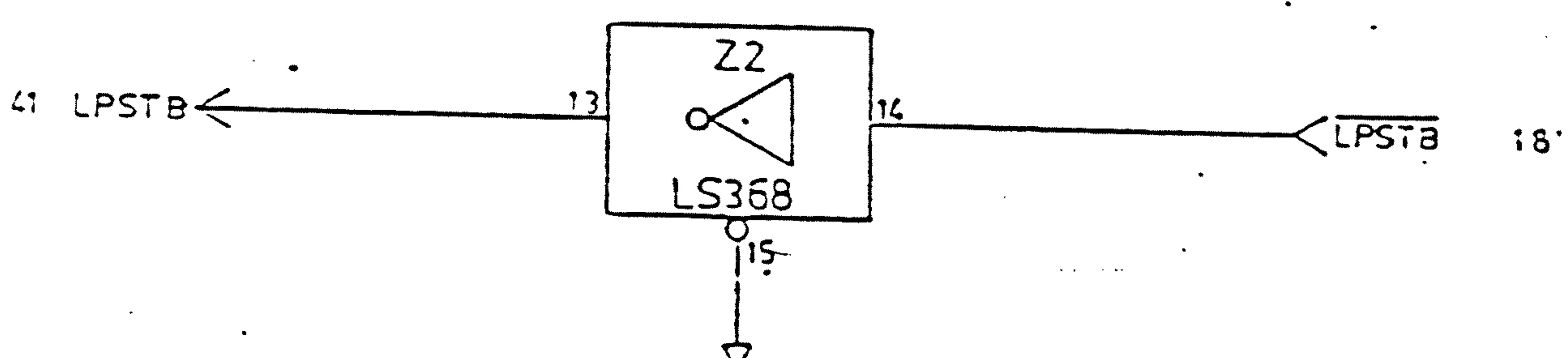
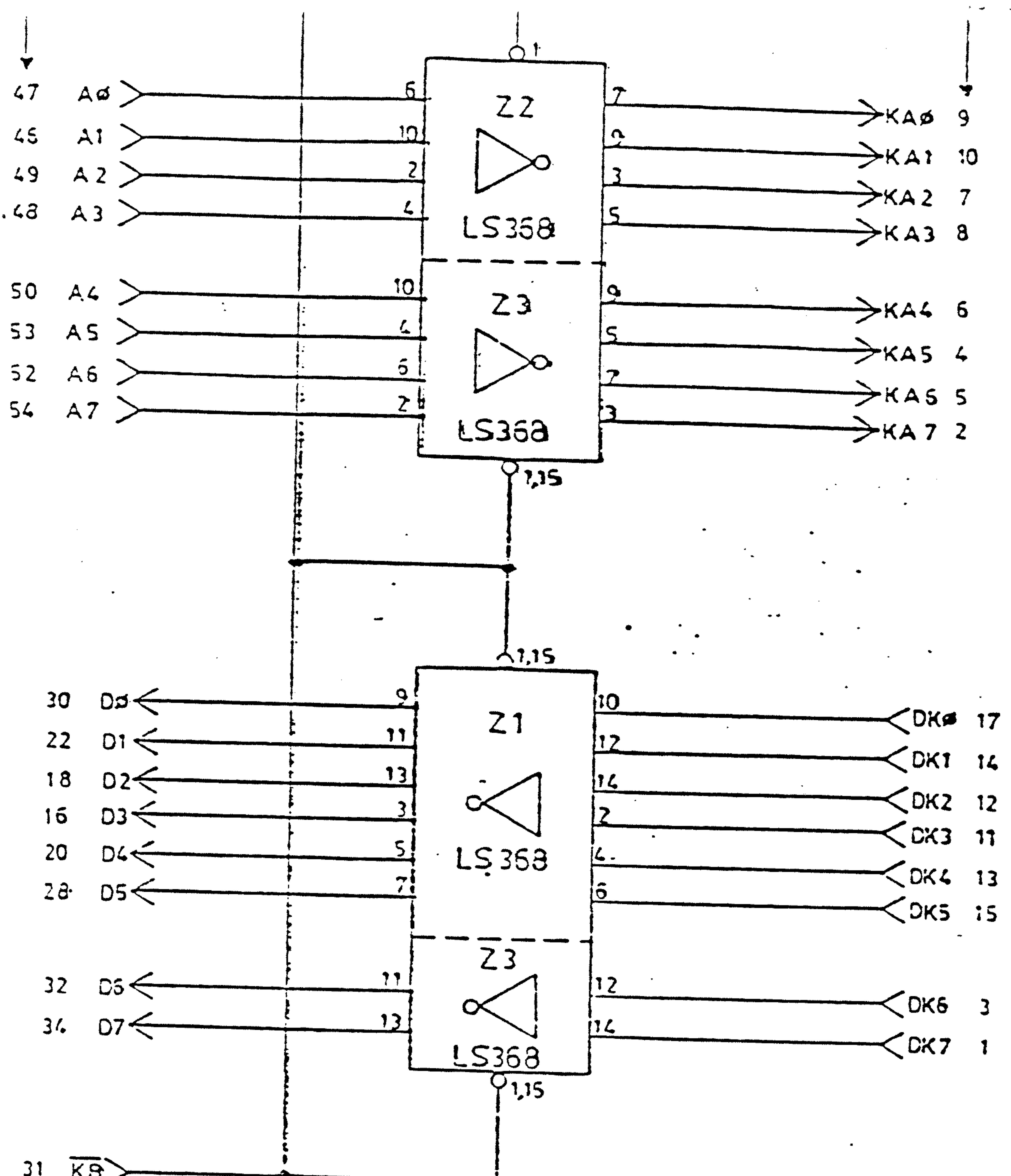
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GENIE III MOTHER BOARD
COMPONENT LAYOUT

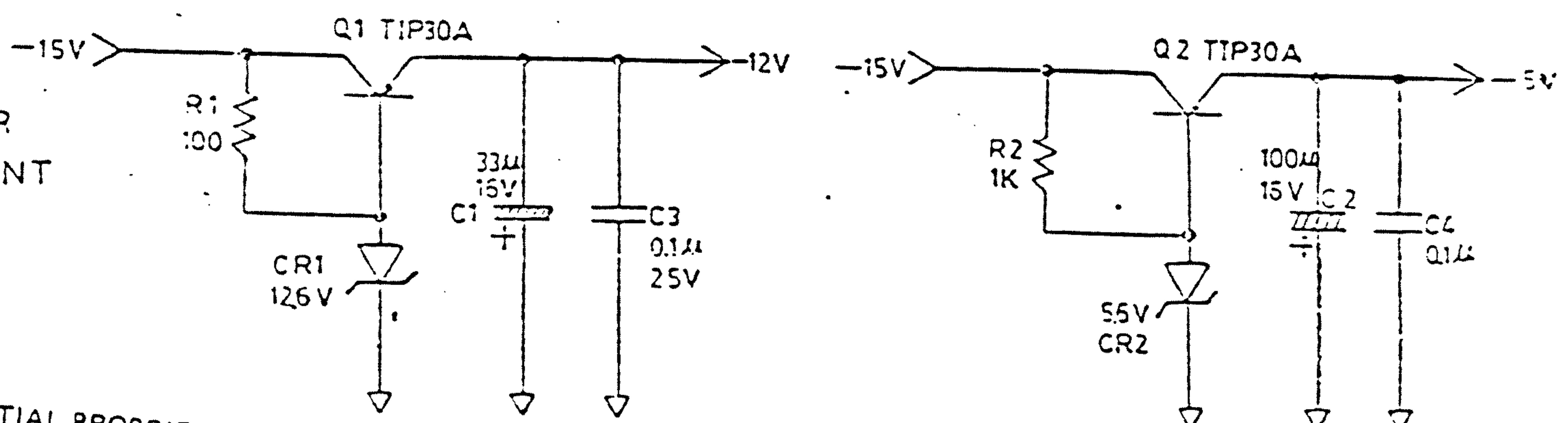
CONNECTOR 20 PIN TO KEYBOARD



10 RESET(SW) ← RESET (SW) 15


5 +5V ← +5V 20
0.1μ CS

3,4,59 GND ← GND 19



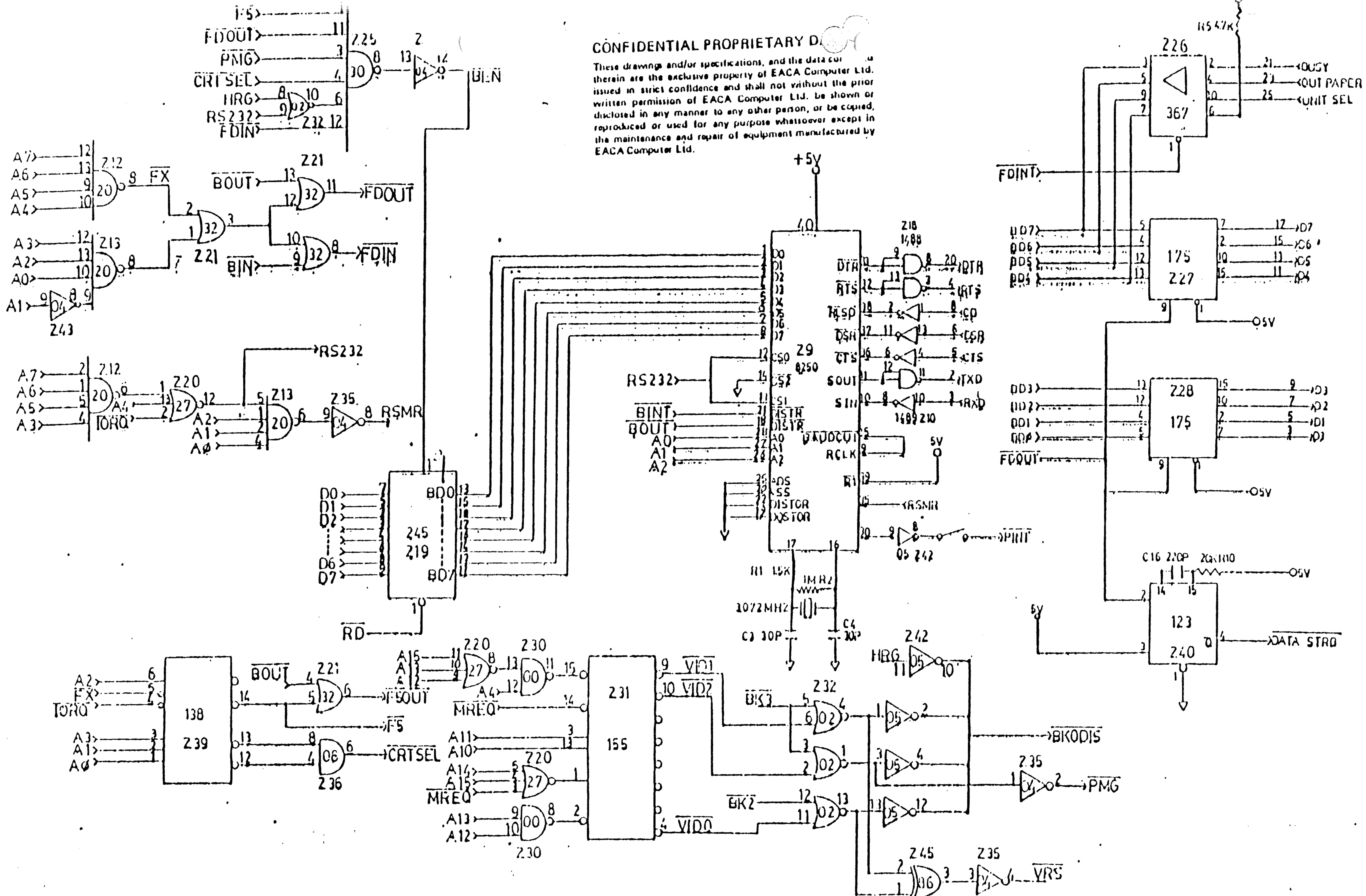
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GENIE III

INTERFACE 1





INTERFACE I

PAGE 1 OF 2


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
GENIE III INTERFACE BOARD



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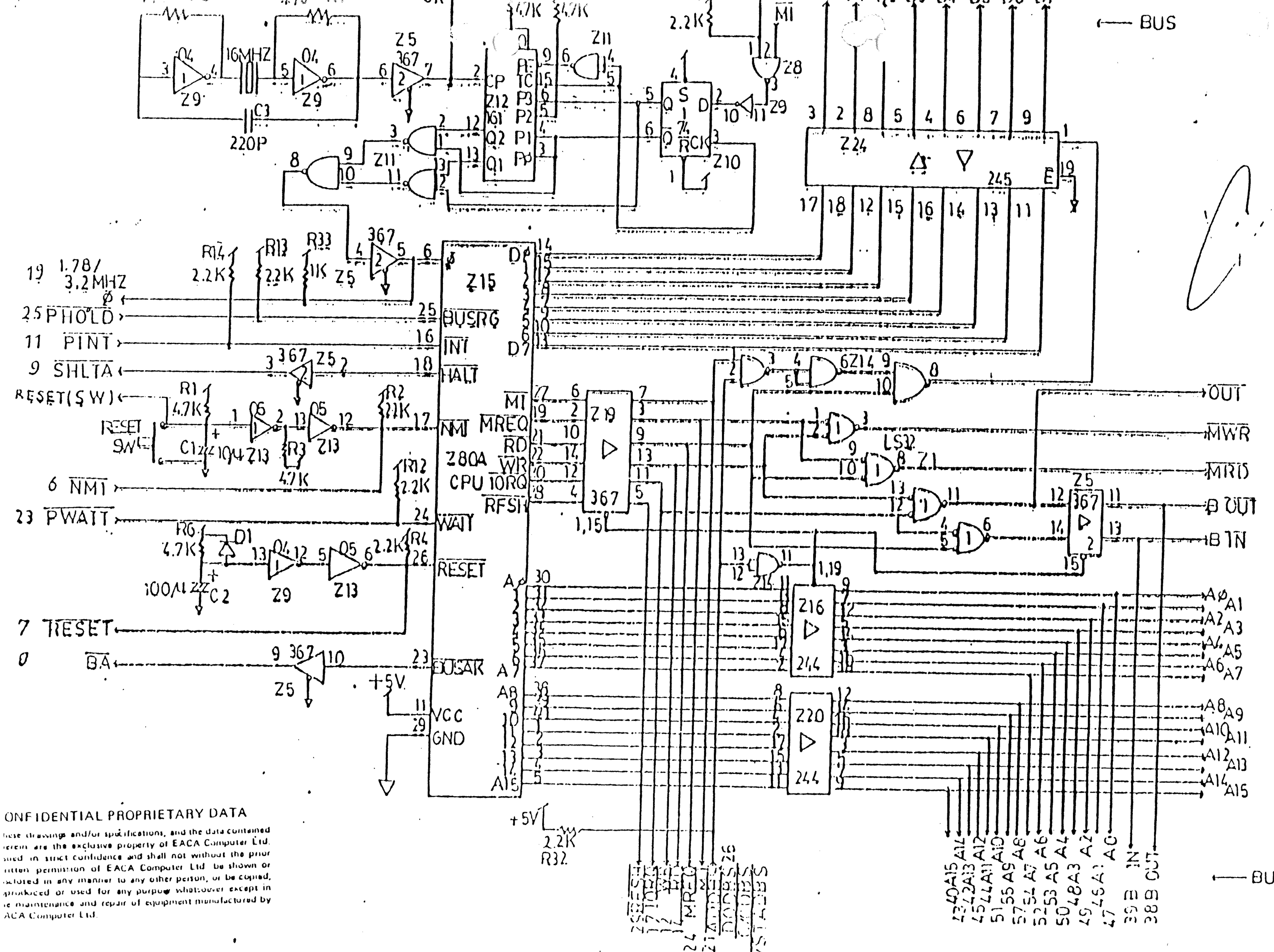
INTERF
PAGE 2



GENIE III CPU BOARD

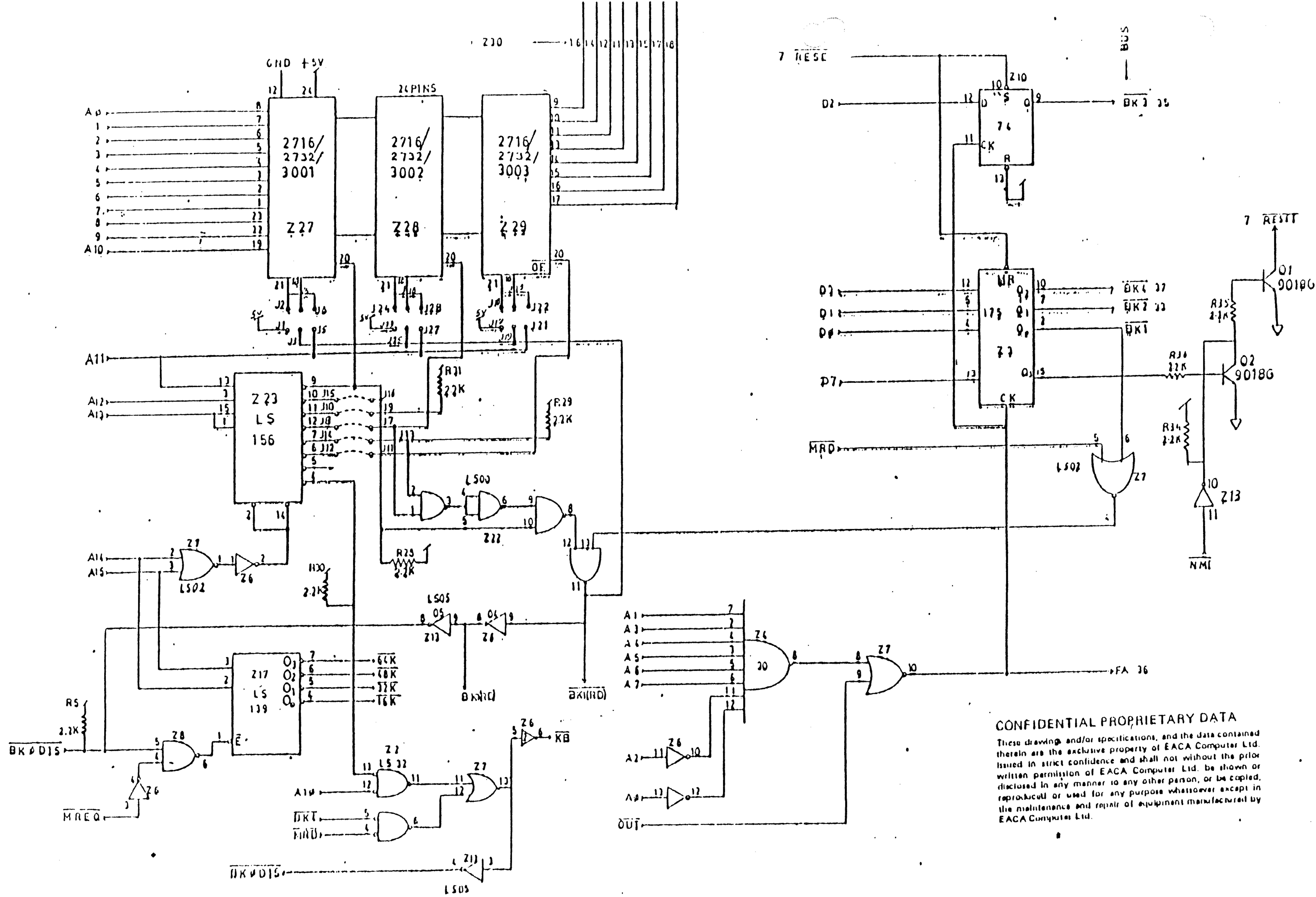
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


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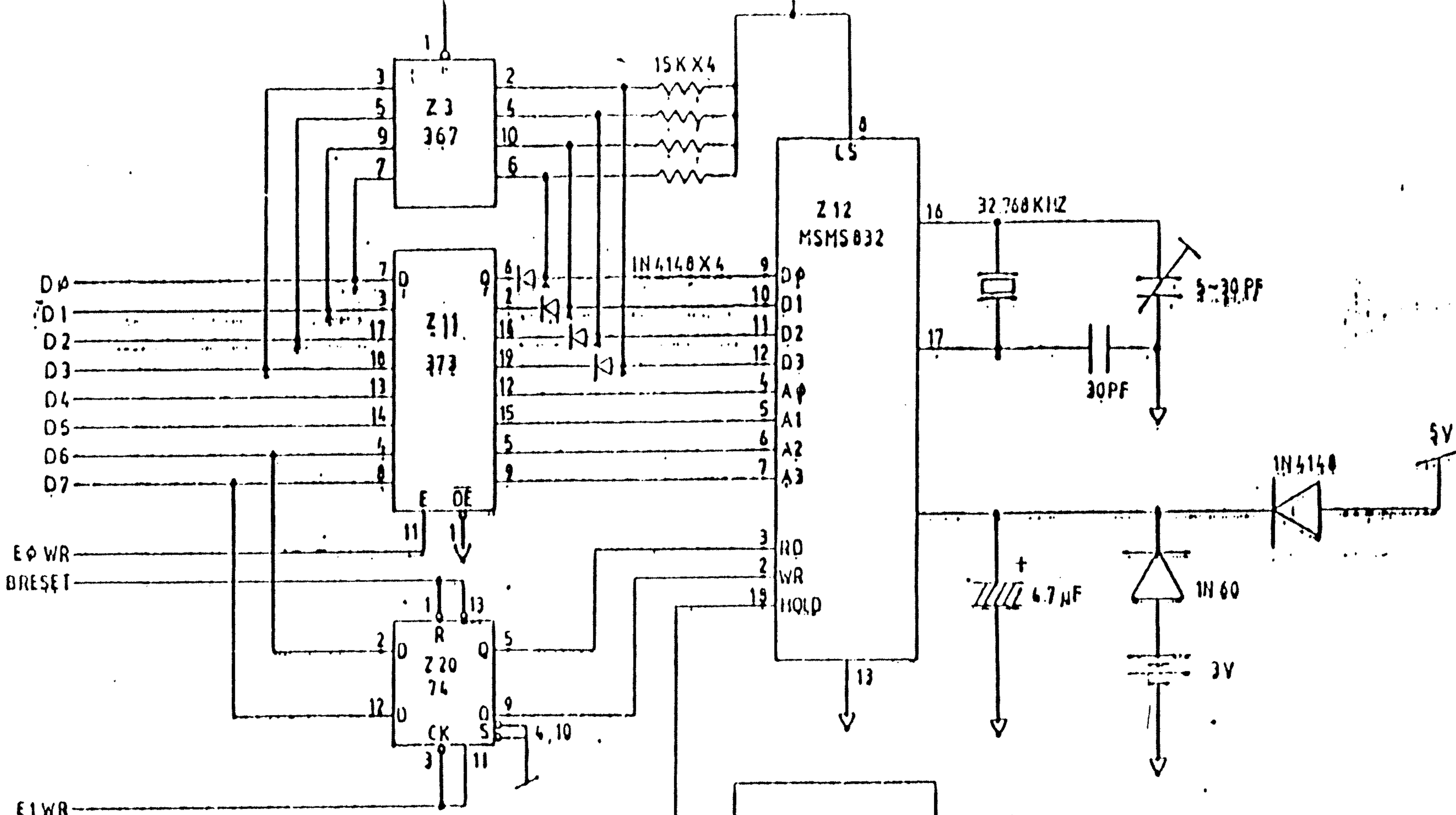
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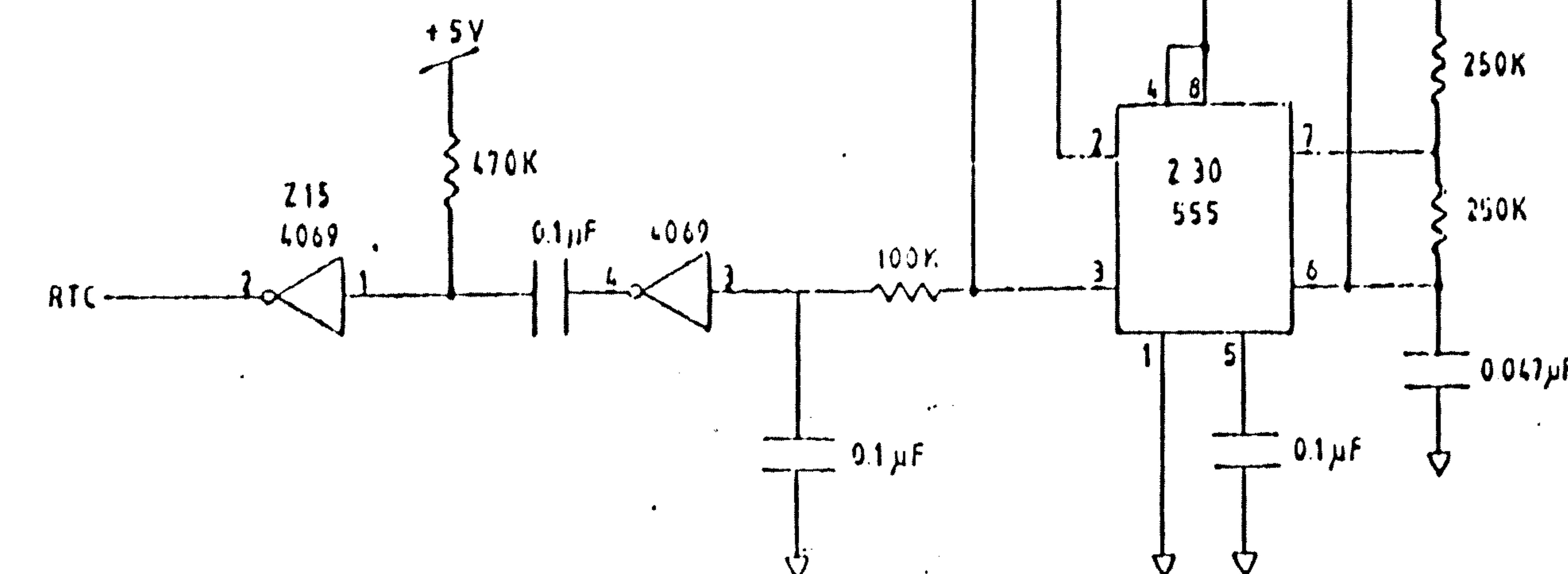
CPU BOARD SHEET 3



EORD




E1WR



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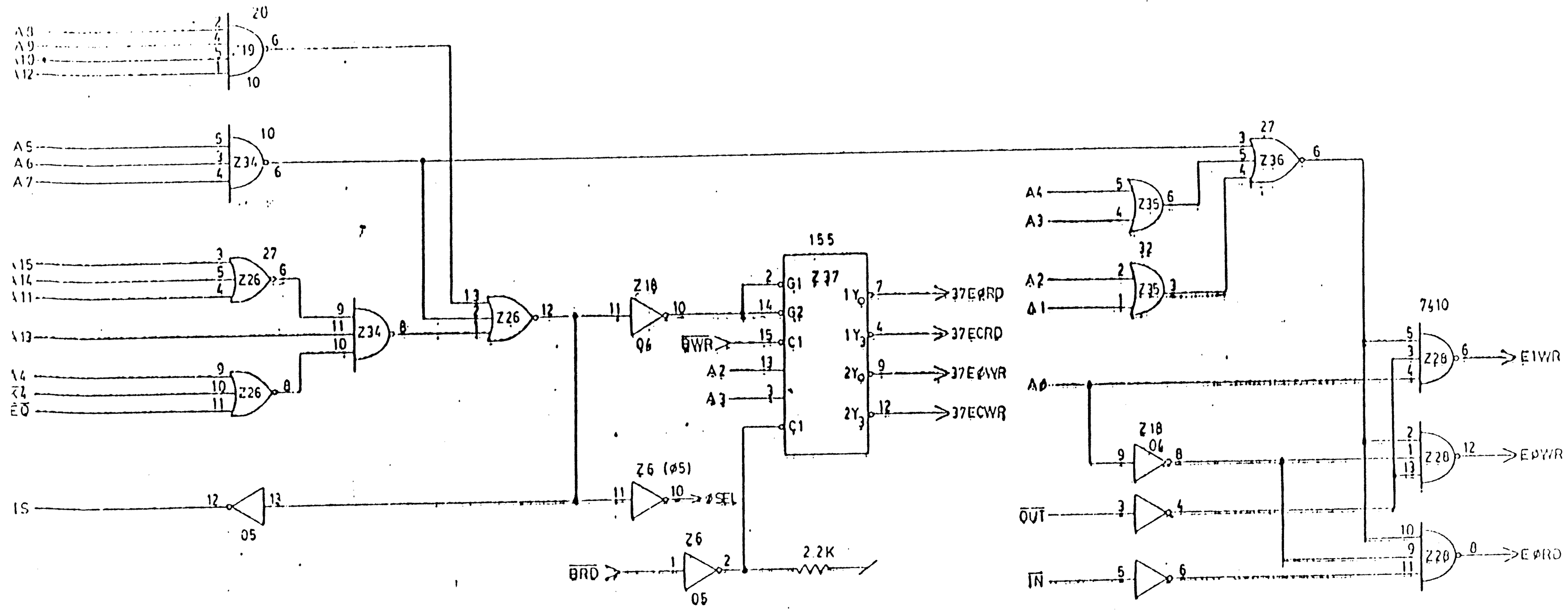
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INTERFACE II
PAGE 2 OF 4



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GENIE III INTERFACE II

PAGE 3 OF 4

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